



# TRANSMITTAL FORM

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		Application Number	10/671,016
		Filing Date	9/25/2003
		First Named Inventor	Dupuis, Timothy J.
		Group Art Unit	2817
		Examiner Name	
Total Number of Pages in This Submission	4	Attorney Docket Number	SIL.P0061

## ENCLOSURES (check all that apply)

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Remarks		

## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Johnson & Associates Bruce A. Johnson	Customer Number 30163
Signature		
Date	February 12, 2005	

## CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

Typed or printed name	Bruce A. Johnson	
Signature		Date
	February 12, 2005	

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UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Dupuis et al.	
Serial No.: 10/671,016	Group Art Unit:
Filed: 9/25/2003	2817
Title: STACKED RF POWER AMPLIFIER	Examiner:
Attorney Docket No.: SIL.P0061	

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

This Information Disclosure Statement is submitted:

under 37 CFR 1.97(b), or  
(Within three months of filing national application; or date of entry of international application; or before mailing date of first office action on the merits; whichever occurs last)

under 37 CFR 1.97(c) together with either a:  
 (1) Certification under 37 CFR 1.97(e), or  
 (2) a \$180.00 fee under 37 CFR 1.17(p), or  
(After the CFR 1.97(b) time period, but before final action or notice of allowance, whichever occurs first)

under 37 CFR 1.97(d) together with a:  
 Certification under 37 CFR 1.97(e), and  
 a \$180.00 fee under 37 CFR 1.17(p).  
(Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

under 37 CFR 1.97(i)  
(Not filed under either § 1.97 or § 1.98. IDS to be placed in the file)

Applicant(s) submit herewith Form PTO 1449-Information Disclosure Citation together with copies, of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

Respectfully submitted,



\_\_\_\_\_  
Bruce A. Johnson  
Attorney for Applicant(s)  
Reg. No. 37361

Date: February 12, 2005

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FORM PTO-1449	ATTY. DOCKET NO. SIL.P0061	SERIAL NO. 10/671,016
	APPLICANT	Dupuis et al.
	FILING DATE	9 25 2003

**REFERENCE DESIGNATION**      **U.S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

**OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)**

		IBM Whitepaper, "SOI Technology: IBM's Next Advance in Chip Design", <a href="http://www.at400.dk/pubfiles/soipaper.pdf">http://www.at400.dk/pubfiles/soipaper.pdf</a> , publication date unkown.
		Chew, K.W., "Impact of Deep N-well Implantation on Substrate Noise Coupling and RF Transistor Performance for Systems-on-a-Chip Integration", 32nd European Solid State Device Research Conference, September 24-26, 2002.
EXAMINER		DATE CONSIDERED